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(a) $n_i = (1.66 \times 10^{15}) (100)^{3/2} \exp \left[\frac{-0.66}{kT} \right]$
 $= (1.66 \times 10^{18}) \exp \left[\frac{-38.37}{kT} \right] \exp \left[\frac{-6}{kT} \right]$
 $2 (86 \times 10^{-3}) (100)^{3/2} \exp \left[\frac{-38.37}{kT} \right] \exp \left[\frac{-6}{kT} \right] n_i = 35.9 \text{ cm}^{-3}$ (b)

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$n_i = (1.66 \times 10^{15}) \left(\frac{300}{300} \right)^{3/2} \exp \left[\frac{-0.66}{kT} \right]$
 $= (8.626 \times 10^{18}) \exp \left[\frac{-12.79}{kT} \right] \exp \left[\frac{-6}{kT} \right]$
 $n_i = 2.40 \times 10^{13} \text{ cm}^{-3}$ (c) $n_i = (1.66 \times 10^{15}) \left(\frac{500}{300} \right)^{3/2} \exp \left[\frac{-0.66}{kT} \right]$
 $= (1.856 \times 10^{19}) \exp \left[\frac{-7.674}{kT} \right] \exp \left[\frac{-6}{kT} \right]$
 $n_i = 8.62 \times 10^{15} \text{ cm}^{-3} \dots$

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A. Neamen Exercise Solutions _____ EX1.8 (V)
VPS = I D R + VD and I D \cong I S exp | D | (VT
| (4 - VD) so 4 = I D (4 \times 10³) + VD \Rightarrow I

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$D = 4 \times 10^3$ and $(V) I_D = (10^{-12}) \exp | D$
 $| (0.026)$ By trial and error, we find $I_D \cong$

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Assume that the BJT in the common-emitter circuit shown in Figure 8.4 has limiting factors of: , and .Neglecting second breakdown effects, determine the minimum value of R_L such that the Q-point of the transistor always stays within the safe operating area for: (a) , and (b) .In each case, determine the maximum collector current and maximum transistor power dissipation.

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