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circuit in terms of the flow of digital signals between hardware registers, and the logical operations performed on those signals. Register-transfer-level abstraction is used in hardware description languages like Verilog and VHDL to create high-level representations of a circuit, from which lower-level representations and ultimately actual wiring can be derived. Design at the RTL level

~~Register-transfer level~~  
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Register Transfer Level  
(RTL) is an abstraction for  
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portions of a design. It is the principle abstraction used for defining electronic systems today and often serves as the golden model in the design and verification flow. The RTL design is usually captured using a hardware description language (HDL) such as Verilog or VHDL.

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approach to digital design, this much-needed book reviews basic design fundamentals before diving into specific details of design optimization.

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~~Logic synthesis - Wikipedia~~  
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Frank Vahid is the author of

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