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Mashiko, K. Maegawa,  
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S. and Inuishi, M. 2001.  
Feasibility of 0.18  $\mu$ m  
SOI CMOS technology  
using hybrid trench  
isolation with high  
resistivity substrate for  
embedded RF/analog  
applications. IEEE ...

**The Design of CMOS  
Radio-Frequency  
Integrated Circuits**  
Discover a fresh  
approach to efficient

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and insight-driven analog integrated circuit design in nanoscale-CMOS with this hands-on guide. Expert authors present a sizing methodology that employs ...

## **Systematic Design of Analog CMOS Circuits**

Design of CMOS digital integrated circuits,

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concentrating on device, circuit, and architectural issues. Analysis and design techniques in custom integrated circuit design, standard cells, memory. Use ...

## **COMP\_ENG 391: CMOS VLSI Circuits Design**

SICK unveiled the first in a new generation of Ruler3000 3D streaming

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cameras. The Ruler3000 line combines SICK's Ranger3 streaming camera with a Class 2 eye-safe laser, pre-selected optics and fixed ...

**SICK unveils new generation of Ruler3000 3D streaming cameras**

Mobix Labs Inc., a fabless RF (radio

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(frequency) component  
company focused on  
next-generation wireless  
technologies, ...

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**Mobix Labs Selected  
as Qualified Partner of  
the Japan External  
Trade Organization**

Work aimed at  
extending the maximum  
frequency capability of  
CMOS technology,  
described in Chapters 5



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and ... which provides a  
tunable linear resistive  
element for integrated  
circuit filter realisation  
...

**Chapter 8: Gallium  
Arsenide Analogue  
Integrated Circuit  
Design Techniques**

Finishing up on the  
topic of CMOS bus  
logic I am going to  
show a ... Switching

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gears I am going to talk  
a bit about the physical  
layout of integrated  
circuits. Shown is an old  
wafer I have ...

## **How CMOS Works: Some Final Words About CMOS**

A top-down guide to the  
design of digital  
integrated circuits.

Reflects industry design  
methods, moving from

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VLSI architecture  
design to CMOS  
fabrication. Practical  
hints and tips, case  
studies, and...

## **Digital Integrated Circuit Design from VLSI Architectures to CMOS Fabrication**

In this paper, a novel  
approach for very stable  
physical unclonable  
function (PUF) is

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presented based on randomly generated via-hole formation using standard CMOS ... integrated circuits. To guarantee ...

**VIA PUF: Ultimately Stable PUF Design using Random Via Formation in Standard CMOS Technology**  
The Belgian research institute expects a

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prototype in standard 28-nm technology to be ready by the end of 2018. At the recent International Microwave Symposium in Philadelphia, Imec announced ...

**Imec Demonstrates  
140-GHz CMOS  
Radar-on-Chip with  
Integrated Antennas**  
Wireless chipset / SoC

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design choices are driven by many orthogonal ... is moved to the RF chip and eventually enables an integrated single chip solution. The integration benefits of CMOS The key to ...

## **System on Chip (SoC) for Short Range Wireless - CMOS versus SiGe**

*Page 14/23*

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This structure also lets the company design membranes that are either flat or curved.

The microphone chip measures 2.5 mm 2 and the diaphragm-has a 1-mm diameter. The company says its CMOS MEMS ...

**Hear this: CMOS  
microphones on a chip  
of six-transistor SRAM-**

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bit cells with an area less than 0.25 square microns—half the size of earlier solutions—using conventional bulk CMOS technology and 45nm design rules. Crolles 2 is the ...

## **Alliance Produces Ultra-Dense SRAM Cell In 45nm CMOS Technology**

reduce design space and



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Complexity. Omnivision also features advanced High Dynamic Range (HDR), which helps ensure high-quality image production in various lighting conditions for numerous ...

**3 New Image Signal Processors Target Power, Speed, and Autonomous Driving**

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Dual work function metal gates are integrated at 17nm spacing between n- and pFETs, highlighting the key benefit of forksheet devices for advanced CMOS area scaling ...  
Alternatively, with a forksheet ...

**Imec demooes  
integrated forksheet  
FETs for 2nm**

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May 27, 2021 (Market  
Insight Reports) --

“CMOS High-speed  
Cameras Market...

Mikrotron, Optronis,  
Integrated Design Tools,  
AOS Technologies,  
Fastec Imaging,  
Weisscam, Del Imaging  
Systems ...

**CMOS High-speed  
Cameras Market Size**

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## **2021 by Consumption, Volume, Average Price, Revenue, Market Share and Trend to 2028**

One technology used in the industry to mitigate this tradeoff is fully depleted silicon-on-insulator (FD-SOI) technology which can operate at 75% lower power as compared to a bulk CMOS process ...

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“We ...

Integrated  
**Lattice Leverages FD-  
SOI for New Low  
Power FPGAs**

With our third generation Keystone DSP design, and the power advantages of 5nm CMOS technology, we are directly addressing our customers' critical needs for low power,

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highly integrated, high

... Integrated

**MaxLinear Showcases**

**Industry's First 5nm**

**CMOS 800G PAM4**

**DSP on TSMC**

**Advanced Process at**

**OFC 2021**

Intevac, Inc. (Nasdaq:  
IVAC) announced today  
that it has received two  
additional Phase 1  
development program

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awards in Photonics.

These awards are ...

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